


Digital method for generating local oscillation signal and numeral controlled oscillator**Publication number:** CN1355655**Publication date:** 2002-06-26**Inventor:** CHEN ZHEN (CN); CHEN GUANGWEN (CN); WANG JING (CN)**Applicant:** HUAWEI TECH CO LTD (CN)**Classification:****- international:** H04B1/16; H04Q7/20; H04B1/16; H04Q7/20; (IPC1-7): H04Q7/20; H04B1/16**- European:****Application number:** CN20001027504 20001123**Priority number(s):** CN20001027504 20001123**Also published as:** CN1166226C (C**Abstract of CN1355655**

The present invention submits a creating method of digital local oscillator signal and digital control oscillator to improve the table look-up amplitude output. The method works as follows: breaking the phase of truncation module output into high address and low address, using high address looking up sine and cosine storage table of high address to obtain the sine and cosine values of high address, using low address as a table looking-up address to look up sine storage table of low address to obtain sine value of low address and carrying out the judgement of low address to obtain the judgement instruction, calculating the sine and cosine values of high address, sine value of low address and judgement instruction as well as outputting the digital local signal. The present invention has the advantages of occupying small chip area, large volume in storage, and low cost of power consumption.

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